

Project 682 U.S. Pro  
09/768504

25/01

	Class	Subclass

Refer to page 10 for more information.

**PATENT NUMBER**

## **U.S. UTILITY Patent Application**

O.I.P.E.

**PATENT DATE**

SCANNED

**Q.A.** QV

CLASS 363	SUBCLASS	ART UNIT 2838	EXAMINER
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/768504		363	21.04	2838	Laxton

Robert Frieznitz  
Charles Devries

## • Activated gate clamp circuit for self driven synchronous rectifiers

TITLE

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

<b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>	<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent, No. _____	(Assistant Examiner)	(Date)	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner)	(Date)	
	(Legal Instruments Examiner)	(Date)	
<b>NOTICE OF ALLOWANCE MAILED</b>			
<b>ISSUE FEE</b>			
	Amount Due	Date Paid	
<b>ISSUE BATCH NUMBER</b>			

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